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	Subclass	
	Class	ISSUE CLASSIFICATION

U.S. **UTILITY** Patent Application

O.I.P.E.	PATENT DATE
SCANNED IK3 Q.A	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/851708	D	324 714	732 RSN	-2858	Brady

APPLICANTS

Indradeep Ghosh

TITLE

Automatic test pattern generation for functional register transfer level circuits using assignment decision diagrams

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)				
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	1				
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
_____ _____				Amount Due	Date Paid
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☐ The terminal ____months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	

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